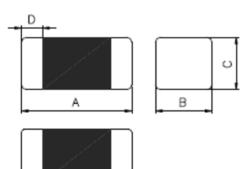


FEATRLRES

- Monolithic inorganic material construction.
- Closed magnetic circuit avoids crosstalk.
- Suitable for reflow soldering.
- Shapes and dimensions follow E.I.A. spec.
- Available in various sizes.
- Excellent solder ability and heat resistance.
- High reliability.
- 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- Low DC resistance structure of electrode to prevent wasteful electric power consumption.

CONFIGRLRATIONS & DIMENSIONS (unit in mm)



Size	Α	В	С	D
HCB1005	1.0±0.10	0.5±0.10	0.5±0.10	0.25±0.10

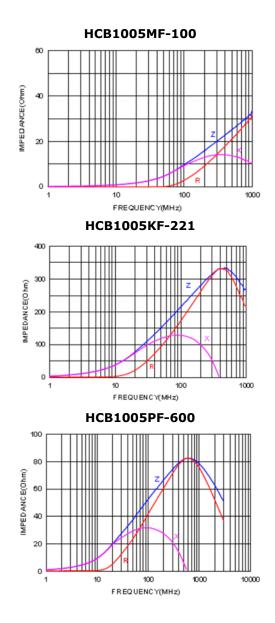
ELECTRICAL CHARACTERISTICS

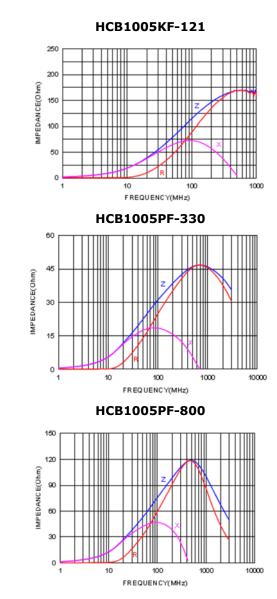
Number	Impedance (Ω)	Test Frequency	DC Resistance (Ω)	Rated Current (mA)
		(MHz)	max.	max.
HCB1005MF-100T25	10±25%	100	0.05	2500
HCB1005KF-121T20	120±25%	100	0.095	2000
HCB1005KF-221T15	220±25%	100	0.15	1500
HCB1005PF-330T30	33±25%	100	0.022	3000
HCB1005PF-600T25	60±25%	100	0.032	2500
HCB1005PF-800T23	80±25%	100	0.038	2300

- Rated current: based on temperature rise test
- In compliance with EIA 595



Impedance Frequency Characteristics(Typical)







Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product with taping) 240~+125℃(on board)	
Electrical Performance Test		
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502, Agilent 33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately∆L30%	Saturation DC Current (Isat) will cause L0 to drop $\ \triangle L(\%)$
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise $\triangle T(^{\circ}C)$. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : 125±2°C (Inductor) Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC
Load Humidity		J-STD-020DClassification Reflow Profiles Humidity : $85\pm 2 \times R.H$, Temperature : $85^{\circ}C \pm 2^{\circ}C$ Duration : 1000hrs Min. with 100% rated current
Moisture Resistance	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	 Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 4. Keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock Vibration		OldDClassification Reflow Profiles Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 25±2°C ≤0.5min Step3 : 125±2°C 30±5min Number of cycles : 500 Measured at room temperature after placing for 24±2 hrs Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment : Vibration checker
		Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).
Bending		Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.
Shock	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the expecification value.	TypePeak value (g's)Normal duration (D)Wave formVelocity change (Vi)ft/secSMD5011Half-sine11.3
	exceed the specification value	Lead 50 11 Half-sine 11.3
Solder ability	More than 95% of the terminal electrode should be covered with solder,	Preheat: 150℃,60sec.。 Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ 。



		Flux for lead free: Rosin. 9.5% °		
		Dip time: 4±1sec •		
		Depth: completely cover the termination		
	Depth: completely cover the termination			
		Temperature Temperature(°C) Time(s) ramp/immersion Number of		
Resistance to Soldering Heat		Temperature(°C) Time(s) ramp/immersion Number of and emersion rate heat cycles		
rissistance to containing risat				
		260 ±5 10 ±1 25mm/s ±6 mm/s 1		
		(solder temp)		
		Presenditioning: Bup through IB reflow for 2 times (IBC/ IEDEC, LSTD		
		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD- 020DClassification Reflow Profiles		
		With the component mounted on a PCB with the device to be tested,		
		apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being		
		tested. This force shall be applied for 60 +1 seconds. Also the force shall		
	Appearance : No damage.	be applied gradually as not to apply a shock to the component being tested.		
	Impedance : within±15% of initial value			
	Inductance : within±10% of initial value			
	Q : Shall not exceed the specification value.	DUT T		
	RDC : within ±15% of initial value and shall not			
	exceed the specification value e	wide		
		thick		
		substrate press tool		
	preservor			

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.